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**TO:** United States Patent and Trademark Office  
Washington, DC 20231  
BOX AF

**FROM:** Steven M. Mills

**FAX NO:** 703-308-7722

**DATE:** March 26, 2003

**RE:** U.S. Patent Application

"Method of Fabricating Semiconductor Devices Having Low Dielectric  
Interlayer Insulation Layer," by Jae-Hak Kim, *et al.*

U.S. Serial No.: 09/994,508

U.S. Filing Date: November 27, 2001

Examiner: Steven H. Rao

Group Art Unit: 2814

Our Reference: SAM-0298

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**NUMBER OF PAGES INCLUDING COVER SHEET: 7**

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**REMARKS:**

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PATENT  
Attorney Docket No.: SAM-0298IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Jae-Hak Kim, et al.

Serial No.: 09/994,508

Filing Date: November 27, 2001

Title: METHOD OF FABRICATING SEMICONDUCTOR DEVICES HAVING LOW  
DIELECTRIC INTERLAYER INSULATION LAYER

Examiner: Rao, S.

Group Art Unit: 2814

CERTIFICATE OF FACSIMILE TRANSMISSION

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Assistant Commissioner for Patents

Washington, D.C. 20231

MAR 26 2003

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TRANSMITTAL LETTER

Sir:

Enclosed herewith for filing in the above-identified patent application please find the following listed items:

1. Notice of Appeal in response to final Office Action mailed on September 26, 2002; and
2. Petition for Three Month Extension of Time

In connection with the foregoing matter, please charge a total of \$1250, the Notice of Appeal fee \$320 and Petition for Three Month Extension of Time fee \$930, and any additional fees which may be due, or credit any overpayment, to Deposit Account Number 50-1798. A duplicate copy of this letter is provided for this purpose.

Respectfully submitted,

Date:

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Attorney for Applicants

PATENT  
Attorney Docket No.: SAM-0298

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